

06/02/01
JC951 U.S. PTO

06-04-01

06/02/01
JC997 U.S. PTO

ADAMS & WILKS
ATTORNEYS AND COUNSELORS AT LAW
50 BROADWAY
31st FLOOR
NEW YORK, NEW YORK 10004

BRUCE L. ADAMS
VAN C. WILKS*

JOHN R. BENEFIEL
PAUL R. HOFFMAN
TAKESHI NISHIDA
FRANCO S. DE LIGUORI

* NOT ADMITTED IN NEW YORK
* REGISTERED PATENT AGENT

BY EXPRESS MAIL ON
June 1, 2001
(EK 825 217 138 US)

RIGGS T. STEWART
(1924-1993)

TELEPHONE
(212) 809-3700

FACSIMILE
(212) 809-3704

Box PATENT APPLICATION
ASSISTANT COMMISSIONER FOR PATENTS
Washington, DC 20231

RECORD OF TRANSMITTAL

INVENTORS: (1) Hirofumi HARADA

TITLE: VERTICAL MOS TRANSISTOR AND A METHOD OF MANUFACTURING
THE SAME

DOCKET NO: S004-4310

S I R:

Kindly find enclosed herewith for filing the documents checked below. Any fees or charges not covered by the accompanying check should be charged to our Deposit Account No. 01-0268.

APPLICABLE ITEMS CHECKED:

PATENT APPLICATION (pages 1-21 of specification, 9 claims (pages) 18-21 and abstract of the disclosure, (page) 22).

DESIGN APPLICATION (specification, claim, declaration and power of attorney and photograph(s))

FORMAL DRAWINGS (5 sheets) (NI A4)

INFORMAL DRAWINGS (sheets)

RECORDATION FORM COVER SHEET and ASSIGNMENT
(to be recorded and returned to the undersigned)

PRIORITY CLAIM - applicant(s) claim the benefit of priority of the following application(s):

Japanese Patent Appln. No. 2000-166214 filed June 2, 2000

PRIORITY DOCUMENT(s)

Enclosed Not Enclosed

VERIFIED STATEMENT TO ESTABLISH SMALL ENTITY STATUS

EXPRESS MAIL CERTIFICATION

OTHER -

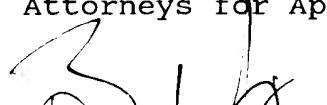
FEE

A check is enclosed for the following official fees:

	Large Entity	Small Entity
<input checked="" type="checkbox"/> Basic Filing Fee	\$ 710.00	\$
<input type="checkbox"/> 0 Independent Claims in Excess of 3	\$	\$
<input type="checkbox"/> 0 Total Claims in Excess of 20	\$	\$
<input type="checkbox"/> Multiple Claim Fee	\$	\$
<input type="checkbox"/> Assignment Recordal Fee	\$	\$
TOTAL	\$ 710.00	\$

Respectfully submitted,

ADAMS & WILKS
Attorneys for Applicant

By: 
Bruce L. Adams
Reg. No. 25,386

50 Broadway
31st Floor
New York, NY 10004
(212) 809-3700

Re: New patent application of Hirofumi HARADA

for VERTICAL MOS TRANSISTOR AND A METHOD OF MANUFACTURING THE SAME

comprising transmittal letter, title, specification (pgs.1-21), 9 claims (pgs.18-21), abstract of the disclosure (pg.22), 5 sheets of formal drawings containing thereon Figs. 1-11, express mail certification and check in the amount \$710.00

Attorney's Docket No: S004-4310

EXPRESS MAIL CERTIFICATION UNDER 37 CFR §1.10

Express Mail Tracking Number: **EK 825 217 138 US**

Date of Deposit: **June 1, 2001**

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR §1.10 on the date indicated above and is addressed to Box PATENT APPLICATION, ASSISTANT COMMISSIONER OF PATENTS, Washington, DC 20231.



Michael Ruas